

|   | L # | Hits     | Search Text                                                                           | DBs                      | Time Stamp       |
|---|-----|----------|---------------------------------------------------------------------------------------|--------------------------|------------------|
| 1 | L1  | 9463     | (latch or latches) and inverter\$1 and transistor\$1 and complementary adj2 signal\$1 | USPAT; EPO; JPO; DERWENT | 2002/10/29 15:27 |
| 2 | L2  | 596      | 1 and skew\$3                                                                         | USPAT; EPO; JPO; DERWENT | 2002/10/29 15:28 |
| 3 | L3  | 22824639 | 2 @pd<=20000101                                                                       | USPAT; EPO; JPO; DERWENT | 2002/10/29 15:30 |
| 4 | L4  | 378      | 2 and @pd<=20000101                                                                   | USPAT; EPO; JPO; DERWENT | 2002/10/29 15:31 |